

Computational Survivalism

Compiler(s) for the End of Moore's Law: a case study

Pierre-Évariste Dagand

Joint work with Darius Mercadier

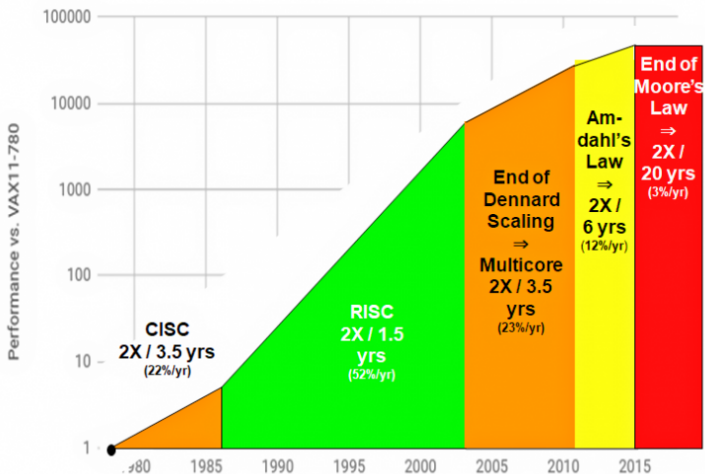
Based on an original idea from Xavier Leroy

LIP6 – CNRS – Inria
Sorbonne Université

The End is Coming

(Maybe)

40 years of Processor Performance



Turing Award Lecture, David Patterson & John Hennessy (2018)

An Escape Hatch

The Way of the Computer Architect:

- Towards domain-specific architectures
- Solving narrow problems
- Delineated by specialized languages
- Gustafson's law: aim for throughput!

What keeps *us* up all night?

- How to organize this diversity?
- Can we retain a “programming continuum”?
- Will PLDI have to go through the next 700 DSLs?

The Usuba Experiment

Setup:

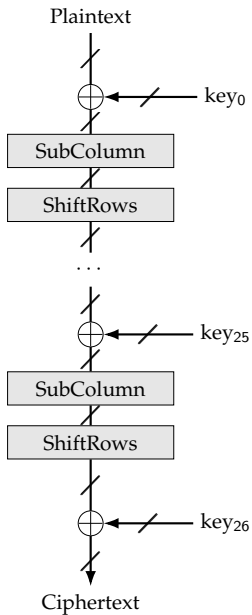
- Domain-specific architecture: SIMD
- Narrow problem: symmetric ciphers
- Specialized language: software circuits

Parameters:

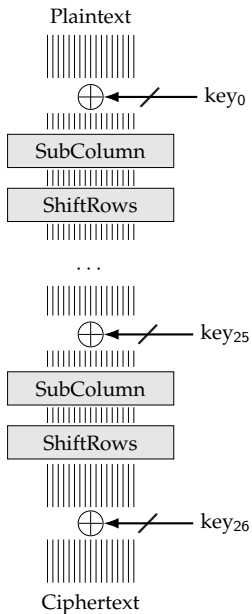
- No runtime, no concurrency
- No memory access *(feature!)*
- Evaluation: optimized reference implementations

The death of optimizing compilers, Daniel J. Bernstein (2015)

Anatomy of a block cipher



Anatomy of a block cipher



Anatomy of a block cipher

Rectangle/SubColumn

The S-box used in RECTANGLE is a 4-bit to 4-bit S-box $S : F_2^4 \rightarrow F_2^4$. The action of this S-box in hexadecimal notation is given by the following table.

x	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
$S(x)$	6	5	C	A	1	E	7	9	B	0	3	D	8	F	4	2

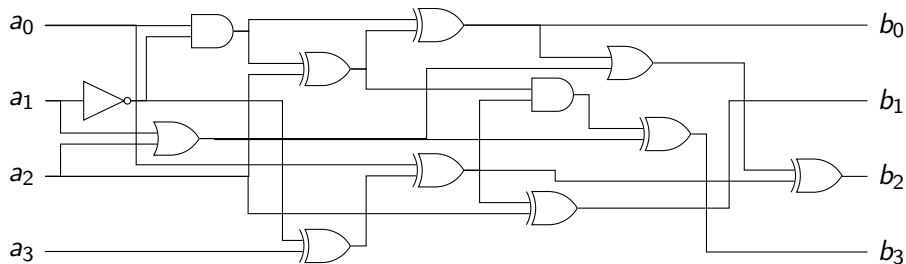
Caution: lookup tables are **strictly forbidden!**

Anatomy of a block cipher

Rectangle/SubColumn

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Anatomy of a block cipher

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$S(x)$	6	5	C	A	1	E	7	9	B	0	3	D	8	F	4	2

```
void SubColumn(__m128i *a0, __m128i *a1,
               __m128i *a2, __m128i *a3) {
    __m128i t1, t2, t3, t5, t6, t8, t9, t11;
    __m128i a0_ = *a0; __m128i a1_ = *a1;
    t1 = ~*a1;          t2 = *a0 & t1;   t3 = *a2 ^ *a3;
    *a0 = t2 ^ t3;     t5 = *a3 | t1;   t6 = a0_ ^ t5;
    *a1 = *a2 ^ t6;   t8 = a1_ ^ *a2; t9 = t3 & t6;
    *a3 = t8 ^ t9;    t11 = *a0 | t8; *a2 = t6 ^ t11;
}
```

Anatomy of a block cipher

Rectangle/SubColumn

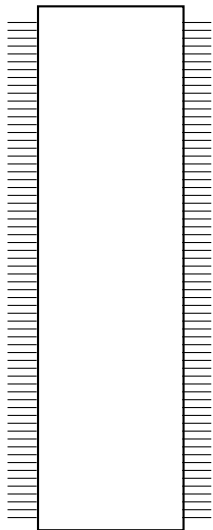
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x	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
$S(x)$	6	5	C	A	1	E	7	9	B	0	3	D	8	F	4	2

```
table SubColumn (a:v4) returns (b:v4) {  
    6, 5, 12, 10, 1, 14, 7, 9, 11, 0, 3, 13, 8, 15, 4, 2  
}
```

Anatomy of a block cipher

Rectangle/ShiftRows

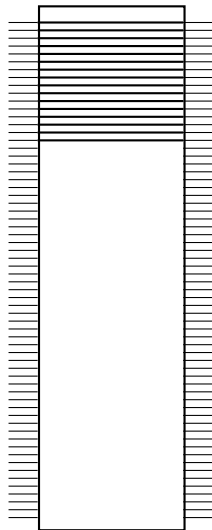


ShiftRows

```
node ShiftRows (input:u16x4)
  returns      (out:u16x4)
```

Anatomy of a block cipher

Rectangle/ShiftRows



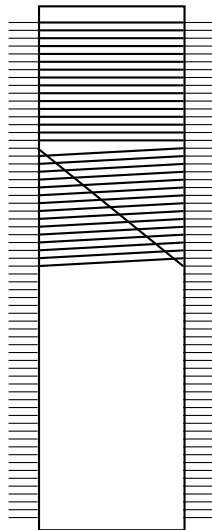
ShiftRows

```
node ShiftRows (input:u16x4)
  returns      (out:u16x4)
let
  out[0] = input[0];

tel
```

Anatomy of a block cipher

Rectangle/ShiftRows



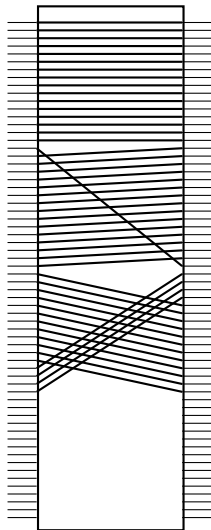
ShiftRows

```
node ShiftRows (input:u16x4)
  returns      (out:u16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;

tel
```

Anatomy of a block cipher

Rectangle/ShiftRows

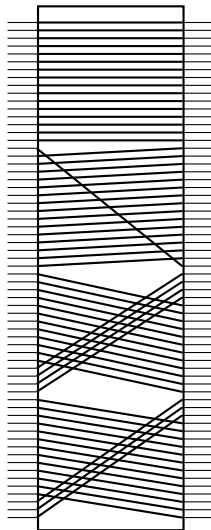


ShiftRows

```
node ShiftRows (input:u16x4)
  returns      (out:u16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
tel
```

Anatomy of a block cipher

Rectangle/ShiftRows

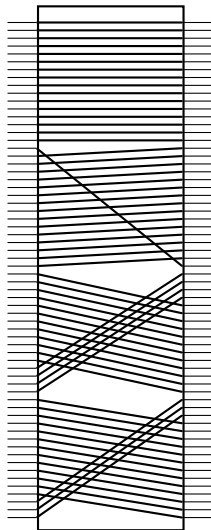


ShiftRows

```
node ShiftRows (input:u16x4)
  returns      (out:u16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```

Anatomy of a block cipher

Rectangle/ShiftRows



ShiftRows

```
void ShiftRows(__m128i a[64]) {  
    int rot[] = { 0, 1, 12, 13 };  
    for (int k = 1; k < 4; k++) {  
        __m128i tmp[16];  
        for (int i = 0; i < 16; i++)  
            tmp[i] = a[k*16+(16+rot[k]+i)%16];  
        for (int i = 0; i < 16; i++)  
            a[k*16+i] = tmp[i];  
    }  
}
```


Anatomy of a block cipher

Rectangle, naïvely

```
void Rectangle(__m128i plain[64], __m128i key[26][64],
              __m128i cipher[64]) {

    for (int i = 0; i < 25; i++) {
        for (int j = 0; j < 64; j++)
            plain[j] ^= key[i][j];
        for (int j = 0; j < 16; j++)
            SubColumn(&plain[j], &plain[j+16],
                    &plain[j+32], &plain[j+48]);
        ShiftRows(plain);
    }
    for (int i = 0; i < 64; i++)
        cipher[i] = plain[i] ^ key[25][i];
}
```

Anatomy of a block cipher

Rectangle, our way

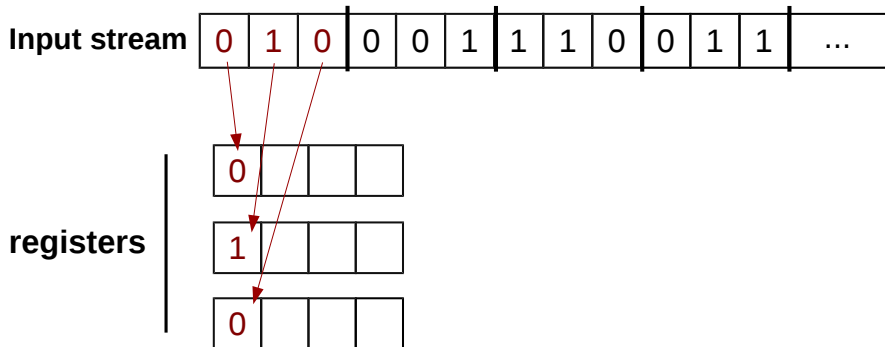
```
node ShiftRows (input:u16x4)
  returns (out:u16x4)
vars
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```

```
table SubColumn (input:v4)
  returns (out:v4) {
    6, 5, 12, 10, 1, 14, 7, 9,
    11, 0, 3, 13, 8, 15, 4, 2
  }
```

```
node Rectangle (plain:u16x4,
  key :u16x4[26])
  returns (cipher:u16x4)
vars
  round : u16x4[26]
let
  round[0] = plain;
  forall i in [0,24] {
    round[i+1] =
      ShiftRows(
        SubColumn(
          round[i] ^ key[i]
        )
      )
  }
  cipher = round[25] ^ key[25]
tel
```

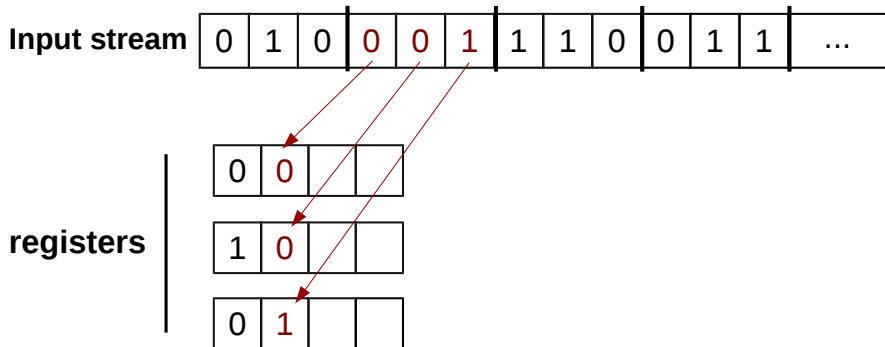
Bitslicing

High-throughput software circuits



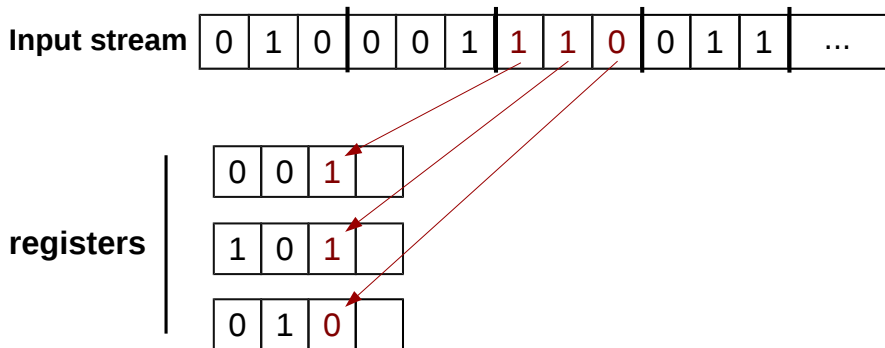
Bitslicing

High-throughput software circuits



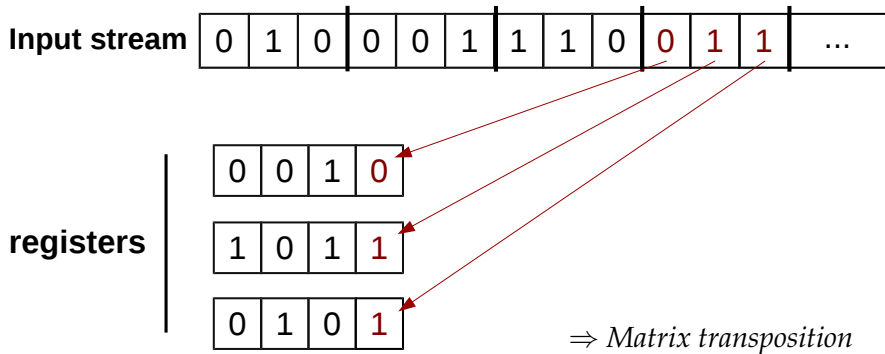
Bitslicing

High-throughput software circuits



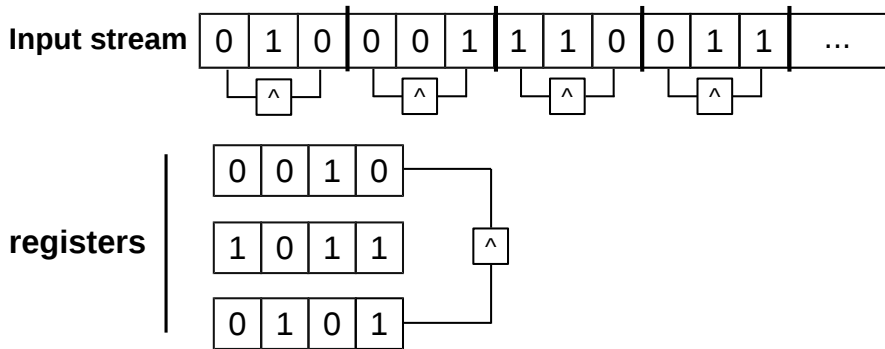
Bitslicing

High-throughput software circuits



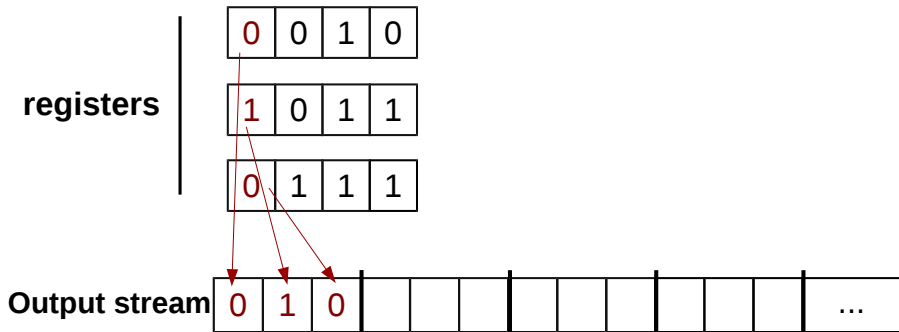
Bitslicing

High-throughput software circuits



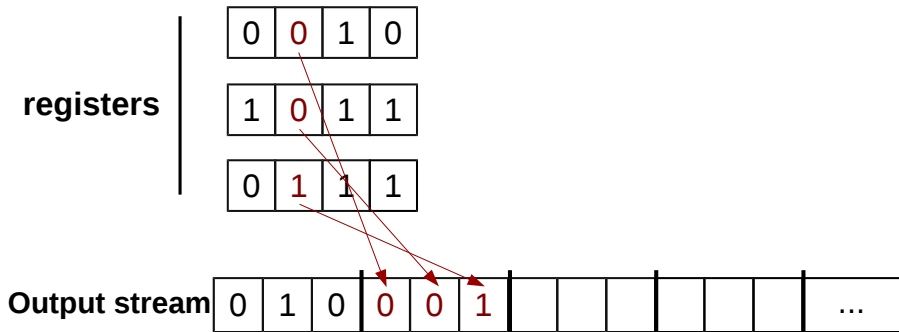
Bitslicing

High-throughput software circuits



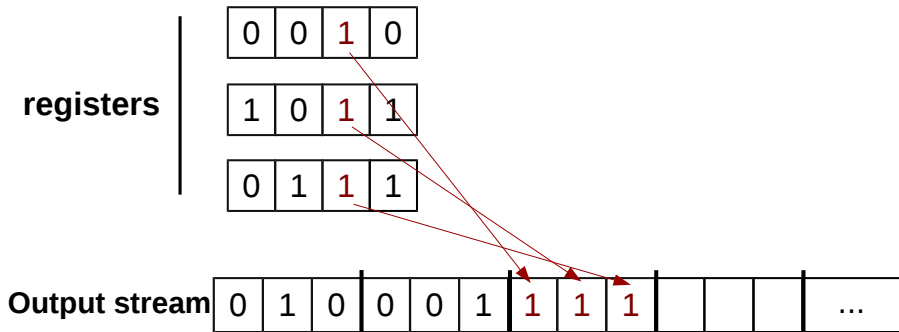
Bitslicing

High-throughput software circuits



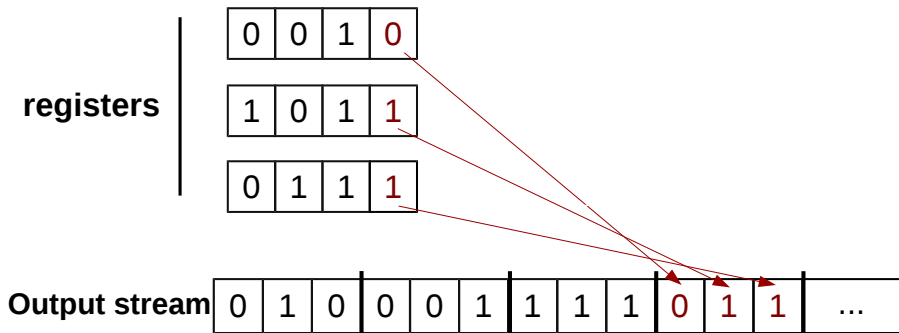
Bitslicing

High-throughput software circuits

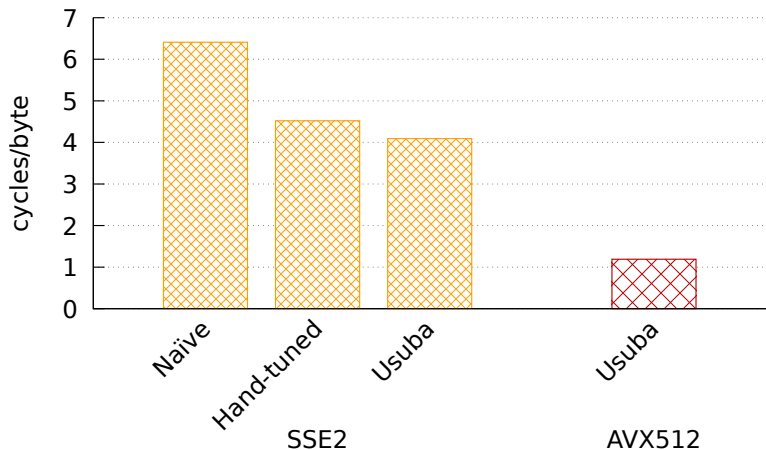


Bitslicing

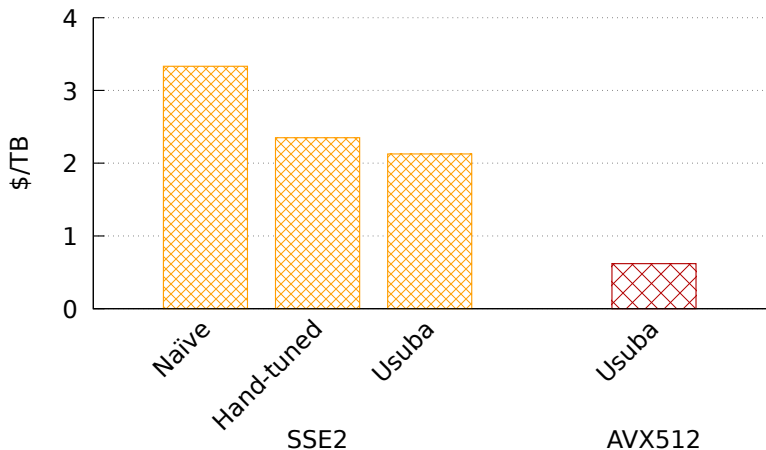
High-throughput software circuits



Man vs. Machine



Man vs. Machine



Anatomy of a block cipher

The Real Thing

```
static void
a1 (
    unsigned long a1,
    unsigned long a2,
    unsigned long a3,
    unsigned long a4,
    unsigned long a5,
    unsigned long a6,
    unsigned long *out1,
    unsigned long *out2,
    unsigned long *out3,
    unsigned long *out4
) {
    unsigned long x1, x2, x3, x4, x5, x6, x7, x8;
    unsigned long x9, x10, x11, x12, x13, x14, x15, x16;
    unsigned long x17, x18, x19, x20, x21, x22, x23, x24;
    unsigned long x25, x26, x27, x28, x29, x30, x31, x32;
    unsigned long x33, x34, x35, x36, x37, x38, x39, x40;
    unsigned long x41, x42, x43, x44, x45, x46, x47, x48;
    unsigned long x49, x50, x51, x52, x53, x54, x55, x56;
    unsigned long x57, x58, x59, x60, x61, x62, x63;

    x1 = ~a4;
    x2 = ~a1;
    x3 = a4 ^ a3;
    x4 = x3 ^ x2;
    x5 = a3 ^ x2;
    x6 = x5 ^ x1;
    x7 = a6 ^ x6;
    x8 = x4 ^ x7;
    x9 = x1 ^ x2;
    x10 = a6 ^ x9;
    x11 = x7 ^ x10;
    x12 = a2 | x11;
    x13 = x8 ^ x12;
    x14 = x9 ^ x13;
    x15 = a5 | x14;
    x16 = x1 ^ x15;
    x17 = ~x14;
    x18 = x17 ^ x3;
    x19 = a2 | x18;
    x20 = x16 ^ x19;
    x21 = a5 | x20;
    x22 = x13 ^ x21;
    *out4 ^= x22;
    x23 = a3 | x4;
    x24 = ~x23;
    x25 = a6 | x24;
    x26 = a5 ^ x25;
    x27 = x1 ^ x8;
    x28 = a2 | x27;
    x29 = x26 ^ x28;
    x30 = x1 | x8;
    x31 = x30 ^ x6;
    x32 = x5 ^ x14;
    x33 = x3 ^ x9;
    x34 = a2 ^ x33;
    x35 = x31 ^ x34;
    x36 = a5 | x35;
    x37 = x29 ^ x36;
    *out1 ^= x37;
    x38 = a3 ^ x10;
    x39 = x38 | x4;
    x40 = a3 ^ x33;
    x41 = x40 ^ x25;
    x42 = a2 | x41;
    x43 = x39 ^ x42;
    x44 = a3 | x26;
    x45 = x44 ^ x14;
    x46 = a1 ^ x8;
    x47 = x46 ^ x20;
    x48 = a2 | x47;
    x49 = x45 ^ x48;
    x50 = a5 ^ x49;
```

```
    x51 = x43 ^ x50;
    *out2 ^= x51;
    x52 = x8 ^ x40;
    x53 = a3 ^ x11;
    x54 = x53 ^ x5;
    x55 = a2 | x54;
    x56 = x52 ^ x55;
    x57 = a6 ^ x4;
    x58 = x57 ^ x38;
    x59 = x13 ^ x56;
    x60 = a2 ^ x59;
    x61 = x58 ^ x60;
    x62 = a5 ^ x61;
    x63 = x56 ^ x62;
    *out3 ^= x63;
}

static void
a2 (
    unsigned long a1,
    unsigned long a2,
    unsigned long a3,
    unsigned long a4,
    unsigned long a5,
    unsigned long a6,
    unsigned long *out1,
    unsigned long *out2,
    unsigned long *out3,
    unsigned long *out4
) {
    unsigned long x1, x2, x3, x4, x5, x6, x7, x8;
    unsigned long x9, x10, x11, x12, x13, x14, x15, x16;
    unsigned long x17, x18, x19, x20, x21, x22, x23, x24;
    unsigned long x25, x26, x27, x28, x29, x30, x31, x32;
    unsigned long x33, x34, x35, x36, x37, x38, x39, x40;
    unsigned long x41, x42, x43, x44, x45, x46, x47, x48;
    unsigned long x49, x50, x51, x52, x53, x54, x55, x56;

    x1 = ~a5;
    x2 = ~a1;
    x3 = a5 ^ a6;
    x4 = x3 ^ x2;
    x5 = x4 ^ a2;
    x6 = a6 | x1;
    x7 = x6 ^ x2;
    x8 = a2 ^ x7;
    x9 = a6 ^ x8;
    x10 = a3 ^ x9;
    x11 = x5 ^ x10;
    x12 = a2 ^ x9;
    x13 = a5 ^ x6;
    x14 = a3 | x13;
    x15 = x12 ^ x14;
    x16 = a1 ^ x15;
    x17 = x11 ^ x16;
    *out2 ^= x17;
    x18 = a5 ^ x1;
    x19 = x13 ^ x19;
    x20 = x13 ^ x19;
    x21 = x20 ^ a2;
    x22 = a6 ^ x4;
    x23 = x22 ^ x17;
    x24 = a3 ^ x23;
    x25 = x21 ^ x24;
    x26 = a6 | x2;
    x27 = a5 ^ x2;
    x28 = a2 ^ x27;
    x29 = x26 ^ x28;
    x30 = x3 ^ x27;
    x31 = x2 ^ x19;
    x32 = a2 ^ x31;
    x33 = x30 ^ x32;
    x34 = a3 ^ x33;
```

Anatomy of a block cipher

The Real Thing

(follows 10 pages of the same...)

Anatomy of a block cipher

The Real Thing

```
unsigned long r25 = p[9];
unsigned long r26 = p[17];
unsigned long r27 = p[25];
unsigned long r28 = p[33];
unsigned long r29 = p[41];
unsigned long r30 = p[49];
unsigned long r31 = p[57];

a1 (e31 ^ k[47], r0 ^ k[11], r1 ^ k[26], r2 ^ k[31], r3 ^ k[13],
   r4 ^ k[41], r8, r16, r122, r130);
a2 (e3 ^ k[27], r4 ^ k[8], r9 ^ k[54], r9 ^ k[48], r7 ^ k[39],
   r8 ^ k[39], r10, r12, r17, r17);
a3 (e7 ^ k[53], r8 ^ k[25], r9 ^ k[31], r10 ^ k[34], r11 ^ k[17],
   r12 ^ k[5], r123, r115, r129, r159);
a4 (e11 ^ k[4], r12 ^ k[9], r13 ^ k[24], r14 ^ k[32], r15 ^ k[40],
   r16 ^ k[20], r125, r119, r19, r10);
a5 (e15 ^ k[36], r16 ^ k[31], r17 ^ k[21], r18 ^ k[8], r19 ^ k[23],
   r20 ^ k[52], r21 ^ k[126], r21);
a6 (e19 ^ k[14], r20 ^ k[29], r21 ^ k[15], r22 ^ k[9], r23 ^ k[35],
   r24 ^ k[30], r13, r128, r110, r118);
a7 (e23 ^ k[2], r24 ^ k[37], r25 ^ k[22], r26 ^ k[0], r27 ^ k[42],
   r28 ^ k[38], r131, r111, r121, r16);
a8 (e27 ^ k[16], r28 ^ k[43], r29 ^ k[44], r30 ^ k[1], r31 ^ k[7],
   r0 ^ k[28], r1 ^ k[18], r1 ^ k[33], r12 ^ k[20], r13 ^ k[20],
   r14 ^ k[48], r8, r16, r22, r30);
a2 (e3 ^ k[34], r4 ^ k[13], r5 ^ k[4], r6 ^ k[55], r7 ^ k[46],
   r8 ^ k[26], r2, r27, r1, r17);
a3 (e7 ^ k[3], r8 ^ k[32], r9 ^ k[40], r10 ^ k[41], r11 ^ k[24],
   r12 ^ k[12], r25, r15, r29, r59);
a4 (e11 ^ k[11], r12 ^ k[5], r13 ^ k[6], r14 ^ k[39], r15 ^ k[47],
   r16 ^ k[27], r25, r19, r9, r20);
a5 (e15 ^ k[42], r16 ^ k[38], r17 ^ k[28], r18 ^ k[15], r19 ^ k[30],
   r20 ^ k[0], r7, r4, r13, r24, r2);
a6 (e19 ^ k[21], r20 ^ k[36], r21 ^ k[31], r22 ^ k[16], r23 ^ k[42],
   r24 ^ k[37], r3, r28, r10, r8);
a7 (e23 ^ k[9], r24 ^ k[4], r25 ^ k[39], r26 ^ k[7], r27 ^ k[49],
   r28 ^ k[45], r31, r1, r21, r6);
a8 (e27 ^ k[23], r28 ^ k[50], r29 ^ k[51], r30 ^ k[8], r31 ^ k[14],
   r10 ^ k[15], r4, r26, r14, r20);
a1 (e31 ^ k[11], r0 ^ k[32], r1 ^ k[47], r2 ^ k[4], r3 ^ k[34],
   r4 ^ k[5], r18, r126, r122, r30);
a2 (e3 ^ k[48], r4 ^ k[27], r5 ^ k[21], r7 ^ k[24], r7 ^ k[3],
   r8 ^ k[40], r12, r127, r11, r117);
a3 (e7 ^ k[17], r8 ^ k[46], r9 ^ k[54], r10 ^ k[55], r11 ^ k[13],
   r12 ^ k[26], r123, r115, r129, r159);
a4 (e11 ^ k[25], r12 ^ k[19], r13 ^ k[20], r14 ^ k[53], r15 ^ k[4],
   r16 ^ k[41], r125, r119, r19, r10);
a5 (e15 ^ k[21], r16 ^ k[52], r17 ^ k[12], r18 ^ k[29], r19 ^ k[41],
   r20 ^ k[14], r17, r113, r124, r42);
a6 (e19 ^ k[35], r20 ^ k[50], r21 ^ k[45], r22 ^ k[30], r23 ^ k[14],
   r24 ^ k[51], r13, r128, r110, r118);
a7 (e23 ^ k[23], r24 ^ k[31], r25 ^ k[43], r26 ^ k[21], r27 ^ k[8],
   r28 ^ k[0], r131, r111, r121, r16);
a8 (e27 ^ k[37], r28 ^ k[9], r29 ^ k[12], r30 ^ k[22], r31 ^ k[28],
   r0 ^ k[25], r10 ^ k[46], r1 ^ k[31], r12 ^ k[13], r13 ^ k[48],
   r14 ^ k[19], r8, r16, r22, r30);
a2 (e3 ^ k[5], r4 ^ k[4], r5 ^ k[32], r6 ^ k[26], r7 ^ k[17],
   r8 ^ k[54], r2, r27, r1, r17);
a3 (e7 ^ k[6], r8 ^ k[3], r9 ^ k[11], r10 ^ k[12], r11 ^ k[27],
   r12 ^ k[40], r25, r19, r9, r20);
a4 (e11 ^ k[39], r12 ^ k[33], r13 ^ k[34], r14 ^ k[10], r15 ^ k[18],
   r16 ^ k[55], r25, r19, r9, r20);
a5 (e15 ^ k[16], r16 ^ k[0], r17 ^ k[15], r18 ^ k[43], r19 ^ k[31],
   r20 ^ k[28], r7, r13, r24, r2);
a6 (e19 ^ k[49], r20 ^ k[9], r21 ^ k[0], r22 ^ k[44], r23 ^ k[15],
   r24 ^ k[39], r3, r28, r10, r8);
a7 (e23 ^ k[37], r24 ^ k[45], r25 ^ k[12], r26 ^ k[35], r27 ^ k[22],
   r28 ^ k[14], r31, r1, r21, r6);
a8 (e27 ^ k[51], r28 ^ k[23], r29 ^ k[13], r30 ^ k[36], r31 ^ k[42],
   r10 ^ k[8], r4, r26, r14, r20);
a1 (e31 ^ k[39], r0 ^ k[3], r1 ^ k[18], r2 ^ k[27], r3 ^ k[5],
   r4 ^ k[33], r18, r116, r122, r130);
```

```
a2 (e3 ^ k[19], r4 ^ k[55], r5 ^ k[46], r6 ^ k[40], r7 ^ k[6],
   r8 ^ k[31], r12, r127, r11, r117);
a3 (e7 ^ k[20], r8 ^ k[17], r9 ^ k[25], r10 ^ k[26], r11 ^ k[41],
   r12 ^ k[54], r123, r115, r129, r159);
a4 (e11 ^ k[53], r12 ^ k[47], r13 ^ k[48], r14 ^ k[24], r15 ^ k[32],
   r16 ^ k[12], r125, r119, r19, r10);
a5 (e15 ^ k[30], r16 ^ k[21], r17 ^ k[15], r18 ^ k[2], r19 ^ k[45],
   r20 ^ k[42], r17, r113, r124, r12);
a6 (e19 ^ k[8], r20 ^ k[23], r21 ^ k[14], r22 ^ k[31], r23 ^ k[29],
   r24 ^ k[52], r13, r128, r110, r118);
a7 (e23 ^ k[51], r24 ^ k[0], r25 ^ k[16], r26 ^ k[49], r27 ^ k[36],
   r28 ^ k[28], r13, r112, r124, r12);
a8 (e27 ^ k[38], r28 ^ k[37], r29 ^ k[7], r30 ^ k[50], r31 ^ k[1],
   r0 ^ k[22], r14, r126, r114, r120);
a1 (e31 ^ k[5], r10 ^ k[17], r11 ^ k[32], r12 ^ k[41], r13 ^ k[19],
   r14 ^ k[47], r8, r16, r22, r30);
a2 (e3 ^ k[33], r14 ^ k[12], r15 ^ k[3], r16 ^ k[54], r17 ^ k[20],
   r18 ^ k[25], r12, r27, r1, r17);
a3 (e7 ^ k[34], r18 ^ k[6], r19 ^ k[39], r20 ^ k[10], r21 ^ k[55],
   r22 ^ k[11], r23, r15, r29, r59);
a4 (e11 ^ k[10], r22 ^ k[4], r23 ^ k[5], r24 ^ k[13], r25 ^ k[46],
   r26 ^ k[26], r25, r19, r9, r20);
a5 (e15 ^ k[44], r26 ^ k[35], r27 ^ k[29], r28 ^ k[16], r29 ^ k[0],
   r30 ^ k[1], r7, r4, r13, r24, r2);
a6 (e19 ^ k[22], r30 ^ k[37], r31 ^ k[28], r12 ^ k[20], r13 ^ k[43],
   r14 ^ k[7], r3, r28, r10, r8);
a7 (e23 ^ k[12], r30 ^ k[36], r31 ^ k[30], r126 ^ k[8], r127 ^ k[50],
   r128 ^ k[42], r31, r1, r21, r6);
a8 (e27 ^ k[52], r30 ^ k[51], r31 ^ k[21], r32 ^ k[9], r33 ^ k[15],
   r34 ^ k[38], r24 ^ k[14], r25 ^ k[30], r126 ^ k[8], r127 ^ k[50],
   r128 ^ k[42], r31, r1, r21, r6);
a1 (e31 ^ k[10], r0 ^ k[6], r1 ^ k[46], r2 ^ k[55], r3 ^ k[33],
   r4 ^ k[4], r18, r116, r122, r130);
a2 (e3 ^ k[47], r4 ^ k[26], r5 ^ k[27], r6 ^ k[11], r7 ^ k[34],
   r8 ^ k[39], r12, r127, r11, r117);
a3 (e7 ^ k[48], r8 ^ k[20], r9 ^ k[53], r10 ^ k[54], r11 ^ k[12],
   r12 ^ k[25], r123, r115, r129, r159);
a4 (e11 ^ k[24], r12 ^ k[39], r13 ^ k[4], r14 ^ k[27], r15 ^ k[31],
   r16 ^ k[40], r125, r119, r19, r10);
a5 (e15 ^ k[31], r16 ^ k[49], r17 ^ k[43], r18 ^ k[30], r19 ^ k[14],
   r20 ^ k[51], r13, r128, r110, r118);
a6 (e19 ^ k[36], r20 ^ k[15], r21 ^ k[42], r22 ^ k[0], r23 ^ k[12],
   r24 ^ k[21], r13, r128, r110, r118);
a7 (e23 ^ k[52], r24 ^ k[28], r25 ^ k[41], r26 ^ k[22], r27 ^ k[9],
   r28 ^ k[7], r131, r111, r121, r16);
a8 (e27 ^ k[17], r28 ^ k[38], r29 ^ k[35], r30 ^ k[23], r31 ^ k[29],
   r0 ^ k[50], r14, r126, r114, r120);
a1 (e31 ^ k[18], r10 ^ k[20], r11 ^ k[3], r12 ^ k[12], r13 ^ k[47],
   r14 ^ k[24], r8, r16, r22, r30);
a2 (e3 ^ k[5], r18 ^ k[34], r19 ^ k[10], r20 ^ k[11], r21 ^ k[26],
   r22 ^ k[41], r15, r116, r122, r130);
a3 (e7 ^ k[13], r12 ^ k[42], r13 ^ k[31], r14 ^ k[48], r15 ^ k[12],
   r16 ^ k[53], r12, r127, r1, r17);
a4 (e11 ^ k[3], r12 ^ k[32], r13 ^ k[33], r14 ^ k[41], r15 ^ k[17],
   r16 ^ k[54], r25, r19, r9, r20);
a5 (e15 ^ k[45], r16 ^ k[8], r17 ^ k[2], r18 ^ k[44], r19 ^ k[28],
   r20 ^ k[50], r120 ^ k[38], r121 ^ k[11], r22 ^ k[14], r23 ^ k[16],
   r24 ^ k[35], r3, r28, r10, r8);
a7 (e23 ^ k[7], r24 ^ k[42], r25 ^ k[31], r26 ^ k[36], r27 ^ k[23],
   r28 ^ k[15], r31, r1, r21, r6);
a8 (e27 ^ k[21], r28 ^ k[52], r29 ^ k[49], r30 ^ k[37], r31 ^ k[43],
   r0 ^ k[19], r12 ^ k[26], r15 ^ k[16]);
a1 (e31 ^ k[6], r0 ^ k[27], r1 ^ k[10], r2 ^ k[19], r3 ^ k[54],
   r4 ^ k[25], r18, r116, r122, r130);
a2 (e3 ^ k[11], r14 ^ k[42], r15 ^ k[31], r16 ^ k[32], r7 ^ k[55],
   r8 ^ k[3], r12, r127, r11, r117);
a3 (e7 ^ k[12], r8 ^ k[4], r9 ^ k[17], r10 ^ k[18], r11 ^ k[33],
   r12 ^ k[48], r25, r19, r9, r20);
a4 (e11 ^ k[20], r12 ^ k[39], r13 ^ k[40], r14 ^ k[48], r15 ^ k[24],
   r16 ^ k[4], r125, r119, r19, r10);
a5 (e15 ^ k[52], r16 ^ k[51], r17 ^ k[12], r18 ^ k[51], r19 ^ k[35],
   r20 ^ k[36], r17, r113, r124, r12);
a6 (e19 ^ k[2], r20 ^ k[45], r21 ^ k[8], r22 ^ k[21], r23 ^ k[23],
   r24 ^ k[42], r13, r128, r110, r118);
```


Anatomy of a block cipher

The Real Thing

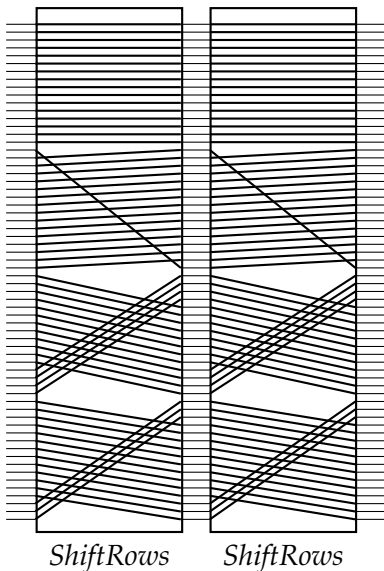
```
a7 (r23 ^ k[14], r24 ^ k[49], r25 ^ k[38], r26 ^ k[43], r27 ^ k[30],
r28 ^ k[2], a13, a11, a12, a13);
a8 (r27 ^ k[28], r28 ^ k[10], r29 ^ k[11], r30 ^ k[44], r31 ^ k[50],
r0 ^ k[16], a14, a126, a114, a120);
a1 (r13 ^ k[20], r10 ^ k[41], r11 ^ k[24], r12 ^ k[33], r13 ^ k[11],
14 ^ k[39], sr8, sr16, sr22, sr30);
a2 (r13 ^ k[25], r4 ^ k[4], r5 ^ k[27], r6 ^ k[46], r7 ^ k[12],
r8 ^ k[17], sr12, sr27, sr1 ^ k[37], sr17);
a3 (r17 ^ k[26], r8 ^ k[55], r9 ^ k[6], r10 ^ k[32], r11 ^ k[47],
r12 ^ k[3], sr23, sr15, sr29, sr5);
a4 (r11 ^ k[34], r12 ^ k[53], r13 ^ k[54], r14 ^ k[5], r15 ^ k[13],
r16 ^ k[19], sr6, sr19, sr9, sr0);
a5 (r15 ^ k[7], r6 ^ k[29], r17 ^ k[23], r18 ^ k[38], r19 ^ k[49],
r20 ^ k[50], sr7, sr13, sr24, sr2);
a6 (r19 ^ k[16], r20 ^ k[1], r21 ^ k[22], r22 ^ k[35], r23 ^ k[37],
r24 ^ k[1], sr3, sr28, sr10, sr18);
a7 (r23 ^ k[28], r24 ^ k[8], r25 ^ k[52], r26 ^ k[2], r27 ^ k[44],
r28 ^ k[36], sr1, sr6, sr2, sr6);
a8 (r27 ^ k[42], r28 ^ k[14], r29 ^ k[15], r30 ^ k[31], r31 ^ k[9],
r0 ^ k[30], sr4, sr26, sr14, sr20);
a1 (r31 ^ k[34], sr0 ^ k[55], r1 ^ k[13], r2 ^ k[47], r3 ^ k[25],
r4 ^ k[53], sr8, sr16, sr22, sr30);
a2 (r3 ^ k[39], r4 ^ k[18], r5 ^ k[41], r6 ^ k[3], r7 ^ k[26],
sr8 ^ k[6], sr12, sr27, sr1 ^ k[37], sr17);
a3 (r7 ^ k[40], r8 ^ k[12], r9 ^ k[20], r10 ^ k[46], r11 ^ k[4],
r12 ^ k[17], sr23, sr15, sr29, sr5);
a4 (r11 ^ k[48], r12 ^ k[10], r13 ^ k[11], r14 ^ k[19], r15 ^ k[27],
r16 ^ k[32], sr25, sr19, sr10);
a5 (r15 ^ k[21], r16 ^ k[43], r17 ^ k[37], r18 ^ k[52], r19 ^ k[8],
r20 ^ k[19], sr7, sr13, sr24, sr2);
a6 (r19 ^ k[30], r20 ^ k[14], r21 ^ k[36], r22 ^ k[49], r23 ^ k[51],
r24 ^ k[15], sr3, sr28, sr10, sr18);
a7 (r23 ^ k[42], r24 ^ k[22], r25 ^ k[7], r26 ^ k[16], r27 ^ k[31],
r28 ^ k[36], sr1, sr6, sr2, sr6);
a8 (r27 ^ k[1], r28 ^ k[28], r29 ^ k[29], r30 ^ k[45], r31 ^ k[23],
r0 ^ k[44], r14, a126, a114, a120);
a1 (r13 ^ k[48], r10 ^ k[12], r11 ^ k[27], r12 ^ k[4], r13 ^ k[39],
14 ^ k[10], sr8, sr16, sr22, sr30);
a2 (r13 ^ k[53], r4 ^ k[32], r5 ^ k[55], r6 ^ k[17], r7 ^ k[40],
r8 ^ k[20], sr12, sr27, sr1 ^ k[37], sr17);
a3 (r17 ^ k[54], r8 ^ k[26], r9 ^ k[34], r10 ^ k[3], r11 ^ k[38],
r12 ^ k[16], sr23, sr15, sr29, sr5);
a4 (r11 ^ k[5], r12 ^ k[24], r13 ^ k[25], r14 ^ k[31], r15 ^ k[41],
r16 ^ k[46], sr25, sr19, sr9, sr0);
a5 (r15 ^ k[35], r16 ^ k[2], r17 ^ k[18], r18 ^ k[7], r19 ^ k[22],
r20 ^ k[37], sr7, sr13, sr24, sr2);
a6 (r19 ^ k[44], r20 ^ k[28], r21 ^ k[50], r22 ^ k[8], r23 ^ k[38],
r24 ^ k[29], sr3, sr28, sr10, sr18);
a7 (r23 ^ k[11], r24 ^ k[36], r25 ^ k[21], r26 ^ k[30], r27 ^ k[45],
r28 ^ k[9], sr31, sr21, sr6);
a8 (r27 ^ k[15], r28 ^ k[42], r29 ^ k[43], r30 ^ k[0], r31 ^ k[37],
r0 ^ k[1], sr4, sr26, sr14, sr20);
a1 (r31 ^ k[5], r0 ^ k[26], r1 ^ k[41], r2 ^ k[18], r3 ^ k[53],
r4 ^ k[24], sr8, sr16, sr22, sr30);
a2 (r3 ^ k[10], r4 ^ k[46], r5 ^ k[12], sr6 ^ k[6], r7 ^ k[54],
sr8 ^ k[4], sr12, sr27, sr1 ^ k[37], sr17);
a3 (r7 ^ k[11], r8 ^ k[40], r9 ^ k[48], r10 ^ k[17], r11 ^ k[32],
r12 ^ k[20], sr23, sr15, sr29, sr5);
a4 (r11 ^ k[19], r12 ^ k[13], r13 ^ k[39], r14 ^ k[47], r15 ^ k[55],
r16 ^ k[3], sr25, sr19, sr10);
a5 (r15 ^ k[49], r16 ^ k[16], r17 ^ k[38], r18 ^ k[21], r19 ^ k[36],
r20 ^ k[37], sr7, sr13, sr24, sr2);
a6 (r19 ^ k[31], r20 ^ k[42], r21 ^ k[9], r22 ^ k[22], r23 ^ k[52],
r24 ^ k[43], sr3, sr28, sr10, sr18);
a7 (r23 ^ k[15], r24 ^ k[50], r25 ^ k[35], r26 ^ k[44], r27 ^ k[0],
r28 ^ k[23], sr31, sr21, sr6);
a8 (r27 ^ k[29], r28 ^ k[1], r29 ^ k[12], r30 ^ k[14], sr1 ^ k[51],
sr0 ^ k[5], sr4, sr26, sr14, sr20);
a1 (r31 ^ k[19], r10 ^ k[40], r11 ^ k[55], r12 ^ k[32], r13 ^ k[10],
r14 ^ k[13], sr8, sr16, sr22, sr30);
a2 (r13 ^ k[24], r4 ^ k[3], r5 ^ k[26], r6 ^ k[20], r7 ^ k[11],
r8 ^ k[48], sr12, sr27, sr1 ^ k[37], sr17);
a3 (r17 ^ k[25], r8 ^ k[54], r9 ^ k[5], r10 ^ k[6], r11 ^ k[46],
r12 ^ k[34], sr23, sr15, sr29, sr5);
```

```
a4 (r11 ^ k[33], r12 ^ k[27], r13 ^ k[53], r14 ^ k[4], r15 ^ k[12],
r16 ^ k[17], sr25, sr19, sr9, sr0);
a5 (r15 ^ k[8], r16 ^ k[30], r17 ^ k[52], r18 ^ k[35], r19 ^ k[50],
r20 ^ k[51], sr7, sr13, sr24, sr2);
a6 (r19 ^ k[11], r20 ^ k[11], r21 ^ k[23], r22 ^ k[36], r23 ^ k[7],
r24 ^ k[2], sr3, sr28, sr10, sr18);
a7 (r23 ^ k[29], r24 ^ k[9], r25 ^ k[49], r26 ^ k[31], r27 ^ k[14],
r28 ^ k[17], sr12, sr27, sr1 ^ k[37], sr17);
a8 (r27 ^ k[43], r28 ^ k[15], r29 ^ k[16], r30 ^ k[28], r31 ^ k[38],
r0 ^ k[0], sr4, sr26, sr14, sr20);
a1 (r31 ^ k[33], r0 ^ k[54], r1 ^ k[12], r2 ^ k[46], r3 ^ k[24],
r4 ^ k[51], sr8, sr16, sr22, sr30);
result = -(r8 ^ c[5]);
result = -(r16 ^ c[3]);
result = -(r22 ^ c[37]);
result = -(r30 ^ c[49]);
if (result == 0)
    return (0);
a2 (r3 ^ k[13], r4 ^ k[17], r5 ^ k[40], r6 ^ k[34], r7 ^ k[25],
r8 ^ k[5], sr12, sr27, sr1 ^ k[37], sr17);
result = -(r127 ^ c[25]);
result = -(r11 ^ c[15]);
result = -(r10 ^ c[11]);
if (result == 0)
    return (0);
a3 (r7 ^ k[39], r8 ^ k[13], r9 ^ k[19], r10 ^ k[20], r11 ^ k[3],
r12 ^ k[48], sr23, sr15, sr29, sr5);
result = -(r23 ^ c[59]);
result = -(r115 ^ c[63]);
result = -(r129 ^ c[41]);
result = -(r5 ^ c[47]);
if (result == 0)
    return (0);
a4 (r11 ^ k[47], r12 ^ k[41], r13 ^ k[10], r14 ^ k[18], r15 ^ k[26],
r16 ^ k[6], sr25, sr19, sr10);
result = -(r119 ^ c[27]);
result = -(r19 ^ c[13]);
result = -(r10 ^ c[7]);
if (result == 0)
    return (0);
a5 (r15 ^ k[22], r16 ^ k[44], r17 ^ k[7], r18 ^ k[49], r19 ^ k[9],
r20 ^ k[38], sr7, sr13, sr24, sr2);
result = -(r17 ^ c[63]);
result = -(r11 ^ c[45]);
result = -(r124 ^ c[1]);
result = -(r12 ^ c[23]);
if (result == 0)
    return (0);
a6 (r19 ^ k[0], r20 ^ k[15], r21 ^ k[37], r22 ^ k[50], r23 ^ k[21],
r24 ^ k[43], sr3, sr28, sr10, sr18);
result = -(r13 ^ c[31]);
result = -(r128 ^ c[33]);
result = -(r110 ^ c[21]);
result = -(r11 ^ c[9]);
if (result == 0)
    return (0);
a7 (r23 ^ k[43], r24 ^ k[23], r25 ^ k[8], r26 ^ k[45], r27 ^ k[28],
r28 ^ k[51], sr31, sr21, sr6);
result = -(r131 ^ c[57]);
result = -(r11 ^ c[29]);
result = -(r121 ^ c[43]);
result = -(r16 ^ c[55]);
if (result == 0)
    return (0);
a8 (r27 ^ k[2], r28 ^ k[29], r29 ^ k[30], r30 ^ k[42], r31 ^ k[52],
r0 ^ k[40], r1 ^ k[14], sr26, sr14, sr20);
result = -(r126 ^ c[17]);
result = -(r11 ^ c[53]);
result = -(r20 ^ c[35]);
if (result == 0)
    return (0);
```

Bitsliced optimization

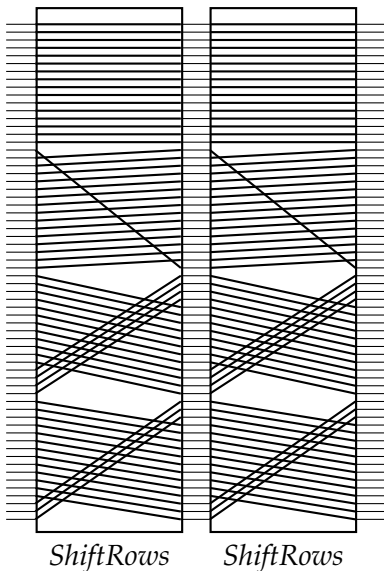
Unrolling & Inlining

```
node ShiftRows_x2 (plain:b64)
    returns (cipher:b64)
let
    forall i in [0,1] {
        plain = ShiftRows(plain)
    }
    cipher = plain
tel
```



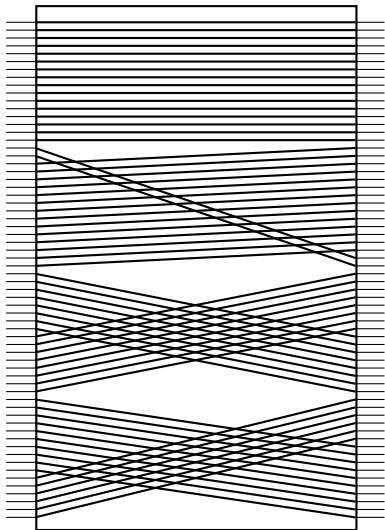
Unrolling & Inlining

```
node ShiftRows_x2 (plain:b64)
    returns (cipher:b64)
let
  forall i in [0,1] {
    tmp[0] = plain[0];
    tmp[1] = plain[1];
    ...
    tmp[16] = plain[17];
    tmp[17] = plain[18];
    ...
    tmp[63] = plain[60];
    plain = tmp;
  }
  cipher = plain
tel
```



Unrolling & Inlining

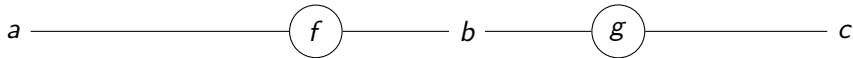
```
node ShiftRows_x2 (plain:b64)
    returns (cipher:b64)
let
    cipher[0] = plain[0];
    cipher[1] = plain[1];
    ...
    cipher[16] = plain[18];
    cipher[17] = plain[19];
    ...
    cipher[63] = plain[57];
tel
```



ShiftRows (x2)

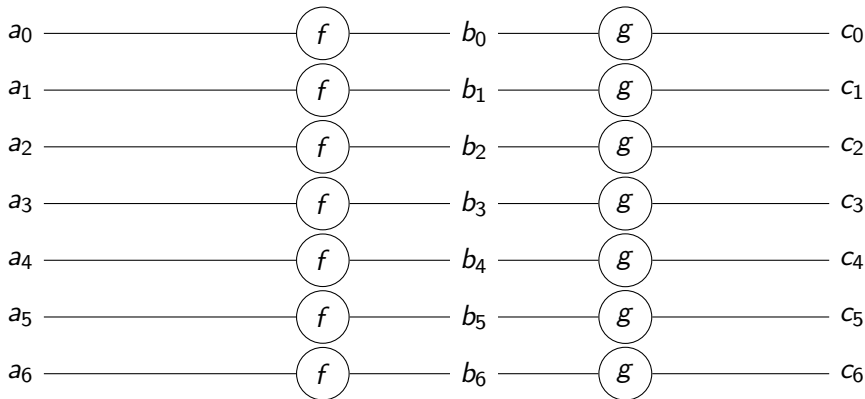
Scheduling bitsliced code

```
// Suppose f: b1 -> b1 and g: b1 -> b1  
node my_cipher (a:b7) returns (c:b7)  
let    b = f(a);  
       c = g(b);    tel
```



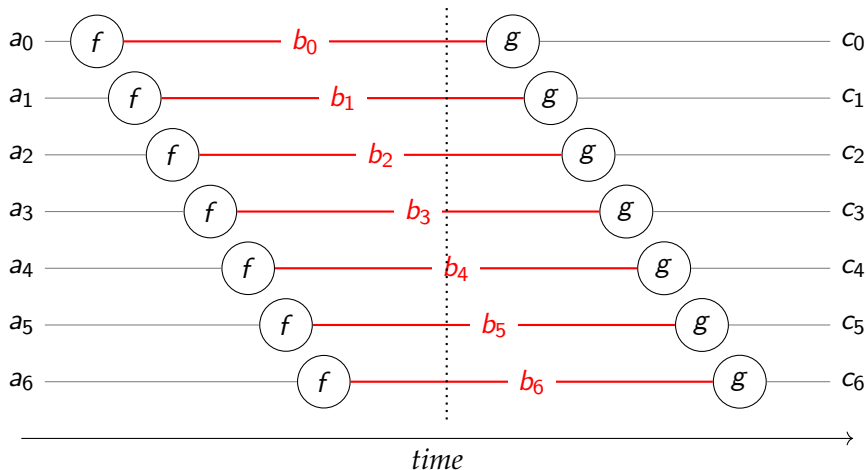
Scheduling bitsliced code

```
// Suppose f: b1 -> b1 and g: b1 -> b1  
node my_cipher (a:b7) returns (c:b7)  
let   b = f(a);  
      c = g(b);   tel
```



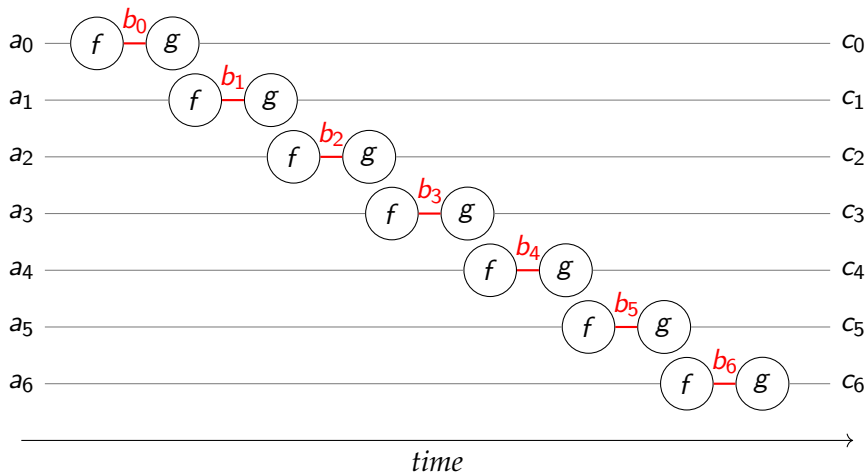
Scheduling bitsliced code

```
// Suppose f: b1 -> b1 and g: b1 -> b1  
node my_cipher (a:b7) returns (c:b7)  
let    b = f(a);  
       c = g(b);    tel
```



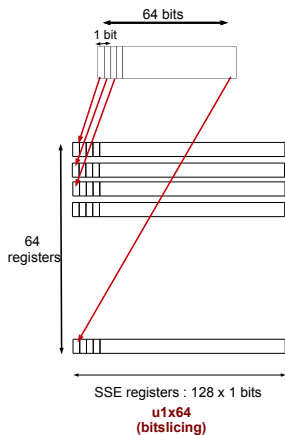
Scheduling bitsliced code

```
// Suppose f: b1 -> b1 and g: b1 -> b1  
node my_cipher (a:b7) returns (c:b7)  
let    b = f(a);  
       c = g(b);    tel
```

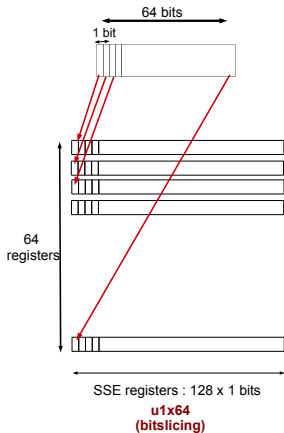
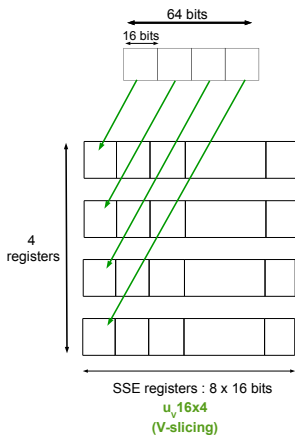


Making larger slices

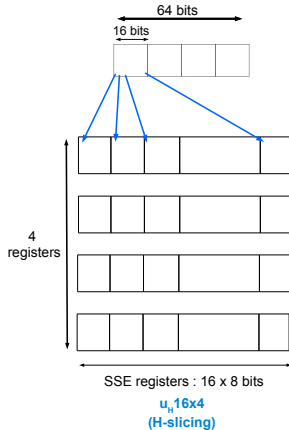
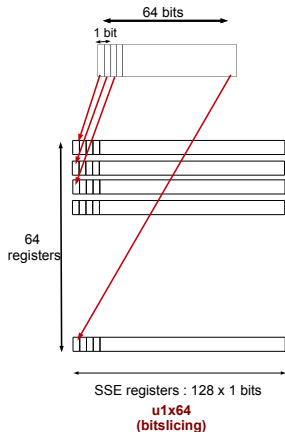
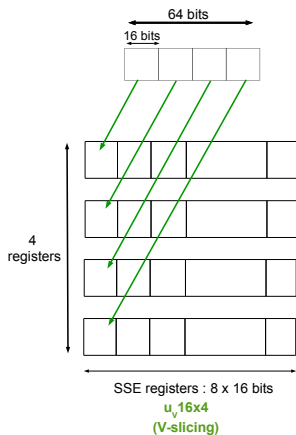
Parallelization strategies



Parallelization strategies



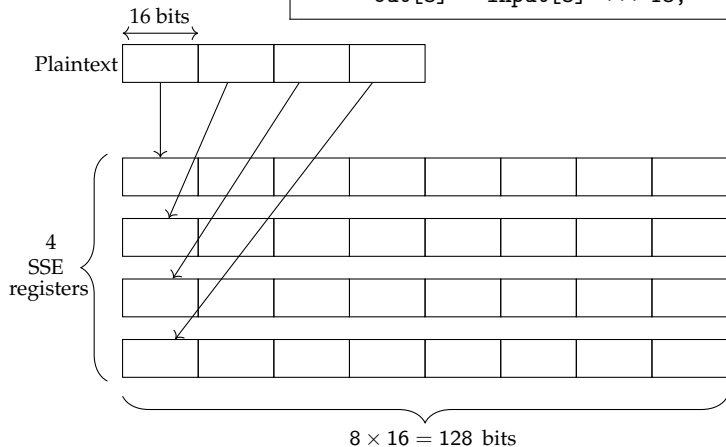
Parallelization strategies



V-slicing

ShiftRows in Vertical mode

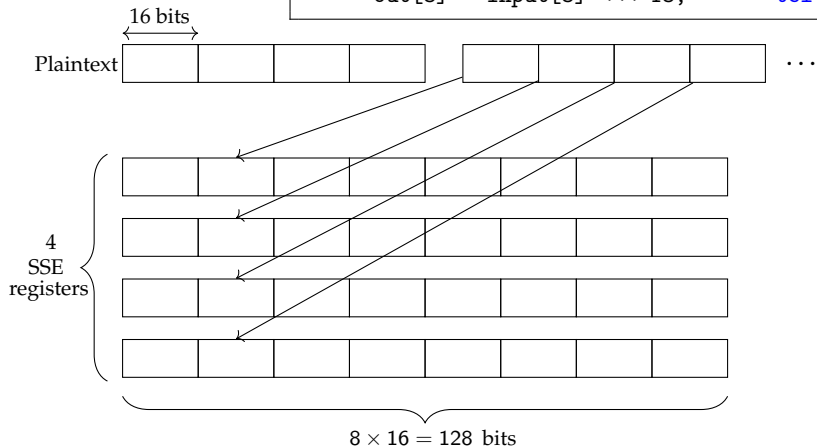
```
node ShiftRows (input:uv16x4) : (out:uv16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```



V-slicing

ShiftRows in Vertical mode

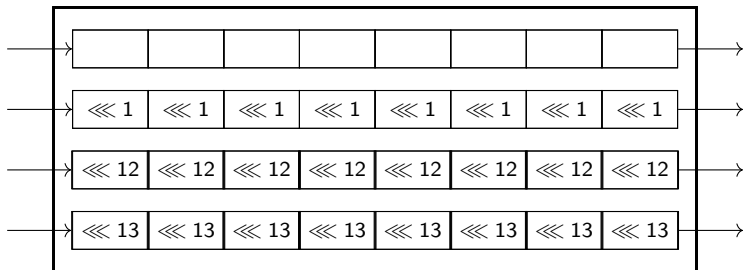
```
node ShiftRows (input:uv16x4) : (out:uv16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```



V-slicing

ShiftRows in Vertical mode

```
node ShiftRows (input:uv16x4) : (out:uv16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;      tel
```



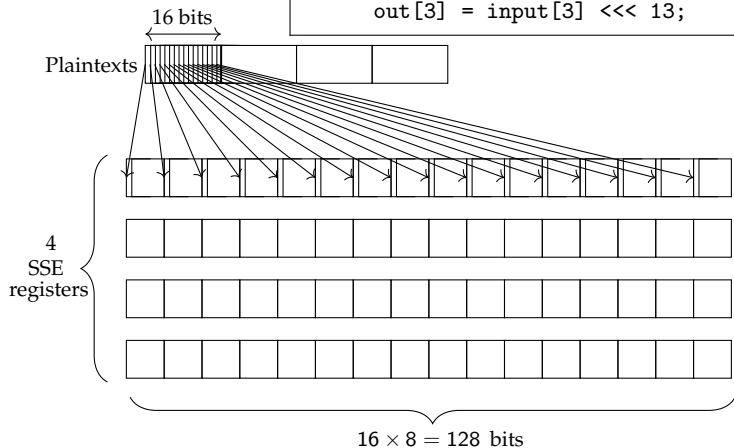
ShiftRows

```
__m128i _mm_sll_epi16 (__m128i a, __m128i count)
```


H-slicing

ShiftRows in Horizontal mode

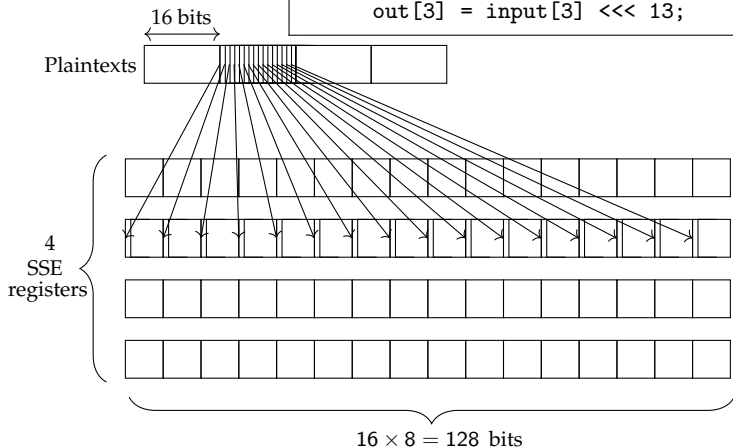
```
node ShiftRows (input:uH16x4) : (out:uH16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```



H-slicing

ShiftRows in Horizontal mode

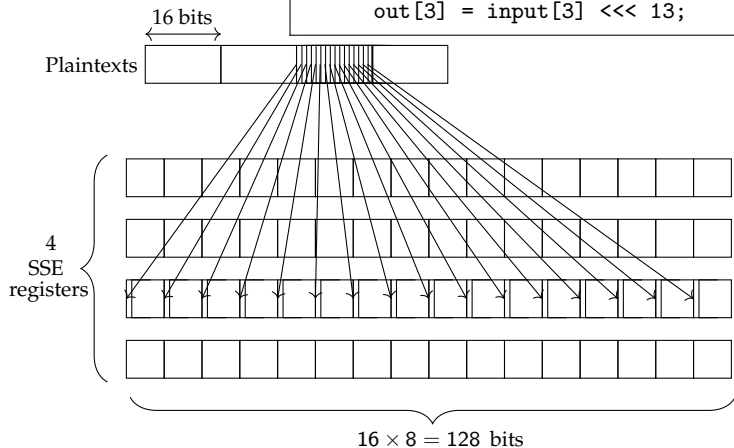
```
node ShiftRows (input:uH16x4) : (out:uH16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```



H-slicing

ShiftRows in Horizontal mode

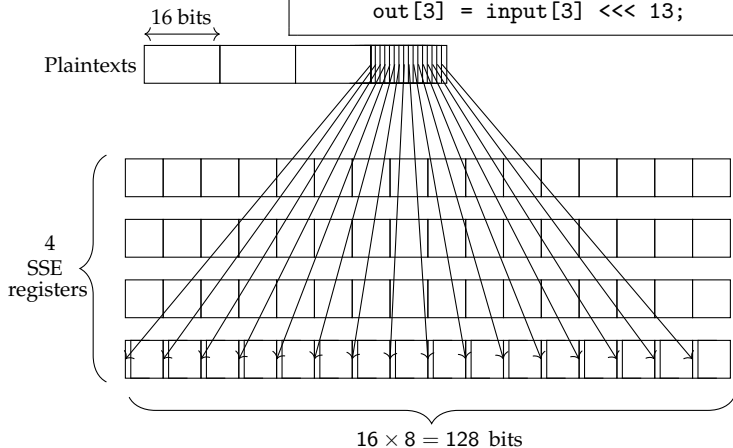
```
node ShiftRows (input:uH16x4) : (out:uH16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```



H-slicing

ShiftRows in Horizontal mode

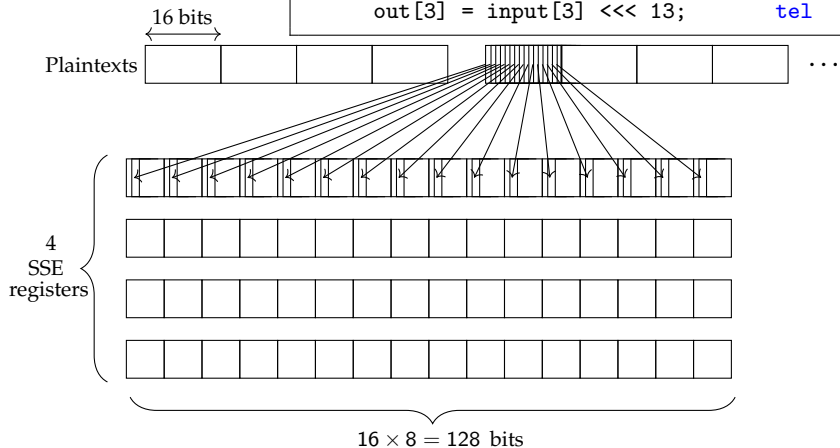
```
node ShiftRows (input:uH16x4) : (out:uH16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```



H-slicing

ShiftRows in Horizontal mode

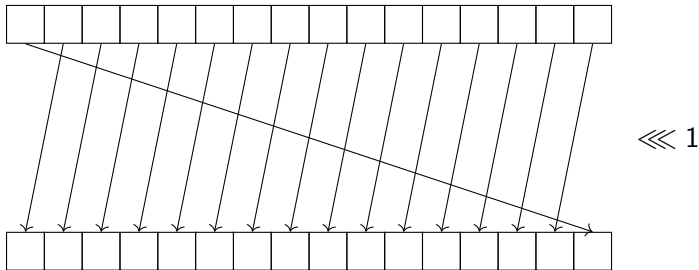
```
node ShiftRows (input:uH16x4) : (out:uH16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```



H-slicing

ShiftRows in Horizontal mode

```
node ShiftRows (input:uH16x4) : (out:uH16x4)
let   out[0] = input[0];
      out[1] = input[1] <<< 1;
      out[2] = input[2] <<< 12;
      out[3] = input[3] <<< 13;      tel
```

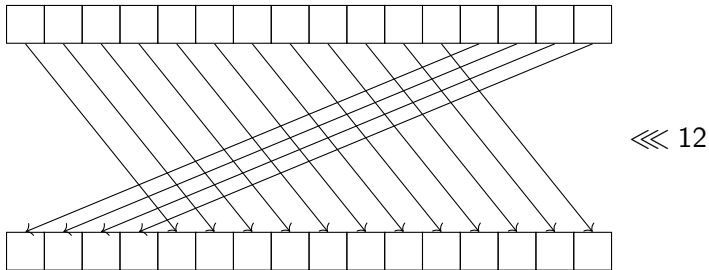


```
__m128i _mm_shuffle_epi8 (__m128i a, __m128i b)
```

H-slicing

ShiftRows in Horizontal mode

```
node ShiftRows (input:uH16x4) : (out:uH16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```

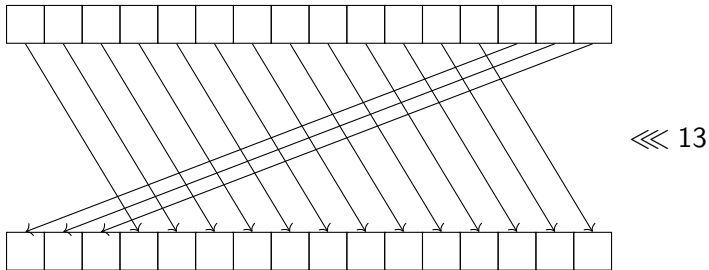


```
__m128i _mm_shuffle_epi8 (__m128i a, __m128i b)
```

H-slicing

ShiftRows in Horizontal mode

```
node ShiftRows (input:uH16x4) : (out:uH16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```



```
__m128i _mm_shuffle_epi8 (__m128i a, __m128i b)
```


Quick Peek at the Language

```
node ShiftRows (input:u16x4)
  returns (out:u16x4)
vars
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```

```
table SubColumn (input:v4)
  returns (out:v4) {
  6, 5, 12, 10, 1, 14, 7, 9,
  11, 0, 3, 13, 8, 15, 4, 2
}
```

```
node Rectangle (plain:u16x4,
  key :u16x4[26])
  returns (cipher:u16x4)
vars
  round : u16x4[26]
let
  round[0] = plain;
  forall i in [0,24] {
    round[i+1] =
      ShiftRows(
        SubColumn(
          round[i] ^ key[i]
        )
      )
  }
  cipher = round[25] ^ key[25]
tel
```

Quick Peek at the Language

```
node ShiftRows (input:uD16x4)
  returns (out:uD16x4)
vars
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```

```
table SubColumn (input:uD'mx4)
  returns (out:uD'mx4) {
  6, 5, 12, 10, 1, 14, 7, 9,
  11, 0, 3, 13, 8, 15, 4, 2
}
```

```
node Rectangle (plain:uD16x4,
  key :uD16x4[26])
  returns (cipher:uD16x4)
vars
  round : uD16x4[26]
let
  round[0] = plain;
  forall i in [0,24] {
    round[i+1] =
      ShiftRows(
        SubColumn(
          round[i] ^ key[i]
        )
      )
  }
  cipher = round[25] ^ key[25]
tel
```

Quick Peek at the Language

```
node ShiftRows (input:u16x4)
  returns (out:u16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```

Quick Peek at the Language

```
node ShiftRows (input:uv16x4)
  returns (out:uv16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```

vslicing

shifts

Quick Peek at the Language

```
node ShiftRows (input:uH16x4)
  returns (out:uH16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```

hslicing

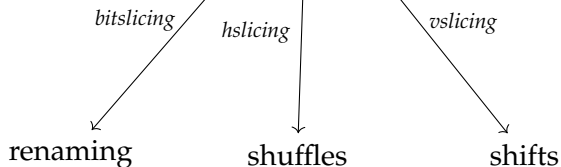
shuffles

vslicing

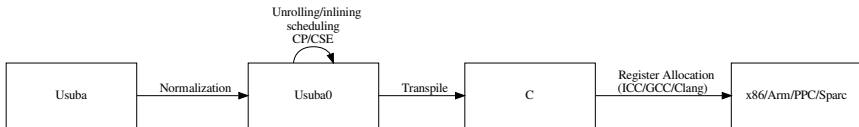
shifts

Quick Peek at the Language

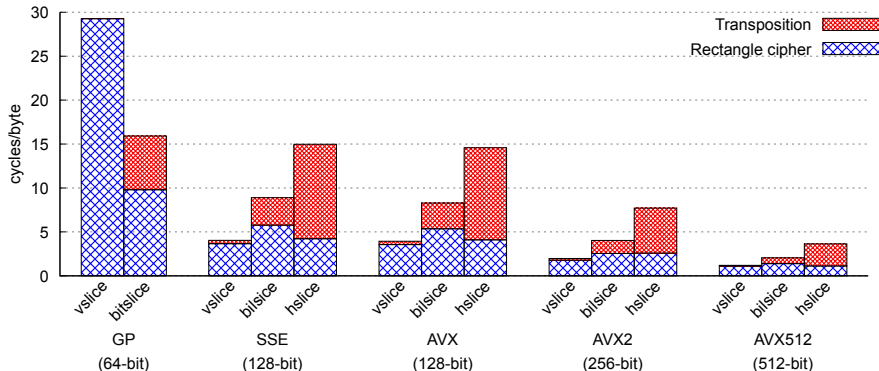
```
node ShiftRows (input:uD1x64)
  returns (out:uD1x64)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```



m-sliced optimization



Monomorphization



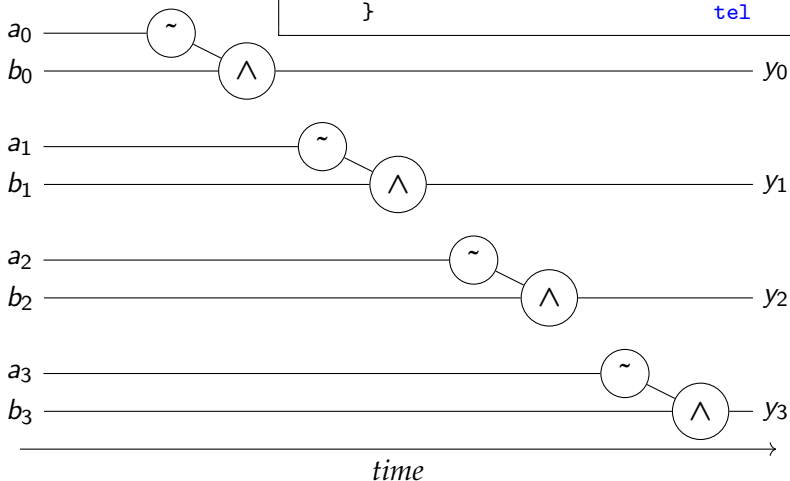
```
node Rectangle (plain : u16x4, key : u16x4[26],  
               cipher : u16x4)
```

```
void RectangleV (__m256i plain[4], __m256i key[26][4],  
                __m256i cipher[4])
```

```
void RectangleB (__m128i plain[64], __m128i key[26][64],  
                __m128i cipher[64])
```

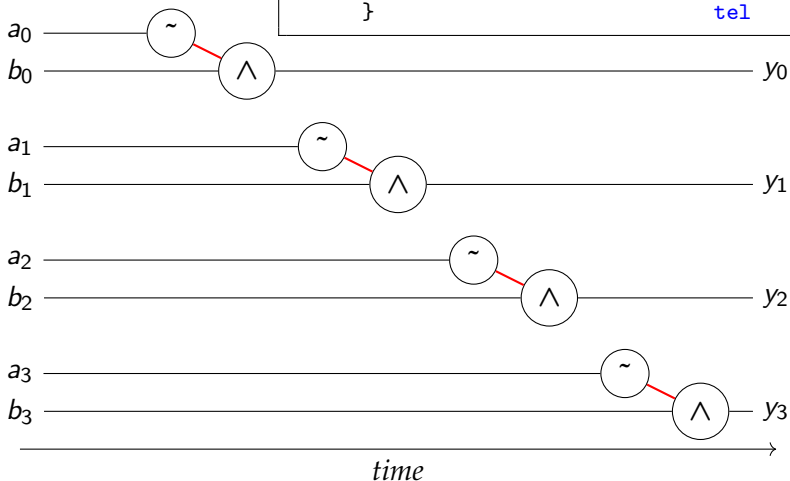

Scheduling m -sliced code

```
node my_cipher (a,b:b4) returns (y:b4)
let forall i in [0, 3] {
    tmp = ~ a[i];
    y[i] = tmp ^ b[i];
}
tel
```



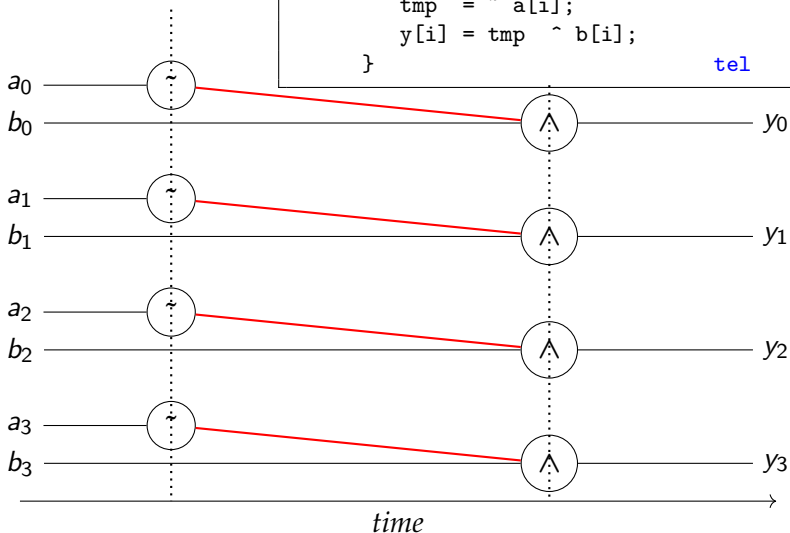
Scheduling m -sliced code

```
node my_cipher (a,b:b4) returns (y:b4)
let forall i in [0, 3] {
    tmp = ~ a[i];
    y[i] = tmp ^ b[i];
}
tel
```



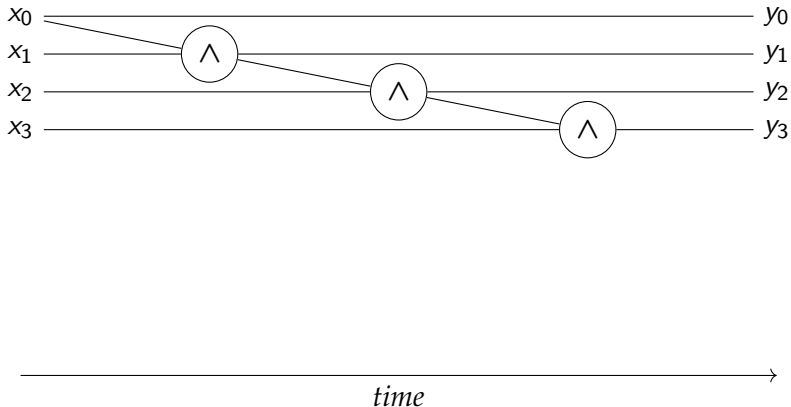
Scheduling m -sliced code

```
node my_cipher (a,b:b4) returns (y:b4)
let forall i in [0, 3] {
    tmp = ~ a[i];
    y[i] = tmp ^ b[i];
}
tel
```



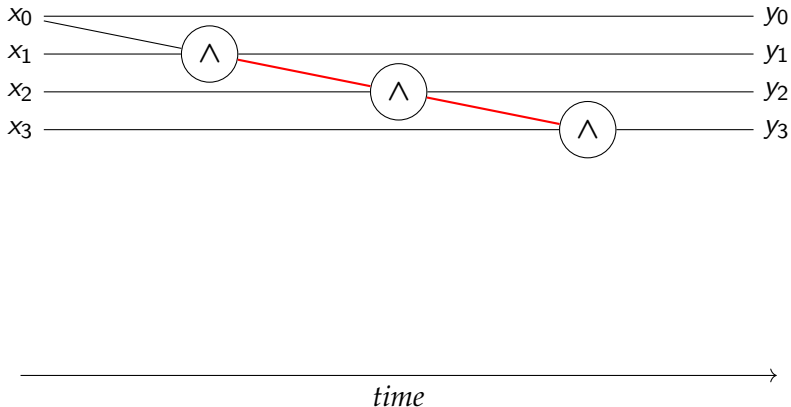
Interleaving

```
node my_cipher (x:b4) returns (y:b4)
let   y[0] = x[0];
      forall i in [1, 3] {
        y[i] = y[i-1] ^ x[i];
      }
tel
```



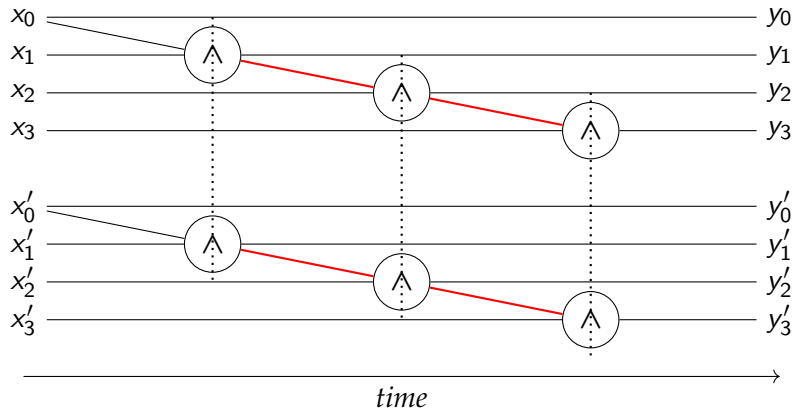
Interleaving

```
node my_cipher (x:b4) returns (y:b4)
let   y[0] = x[0];
      forall i in [1, 3] {
        y[i] = y[i-1] ^ x[i];
      }
tel
```



Interleaving

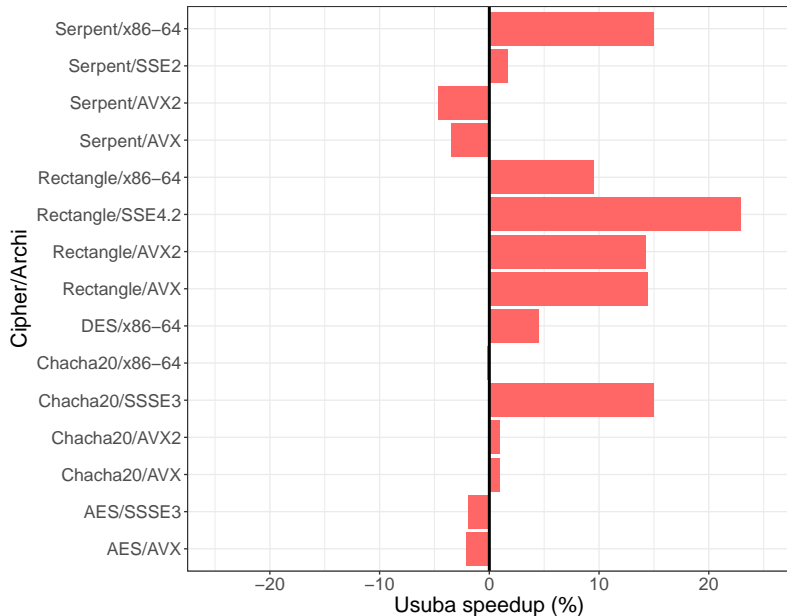
```
node my_cipher (x:b4) returns (y:b4)
let   y[0] = x[0];
      forall i in [1, 3] {
        y[i] = y[i-1] ^ x[i];
      }
tel
```



Evaluation & Conclusion

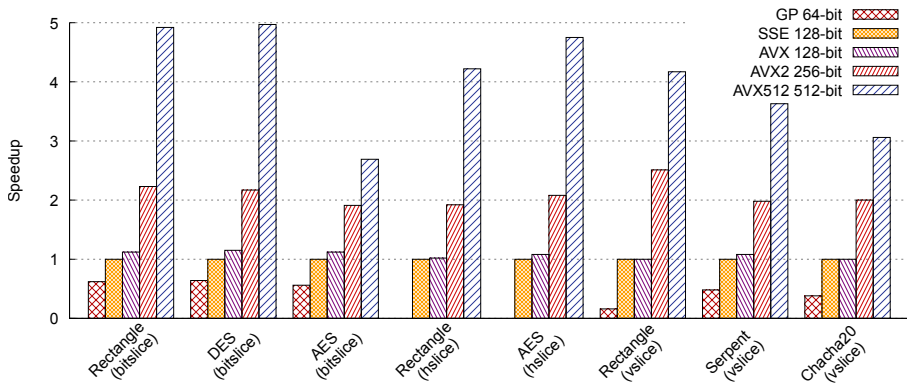
Evaluation

Usuba vs. Reference



Evaluation

Scalability



Conclusion

Usuba:

- High-level description of combinational circuits
- General model of m -slicing
- Generates optimized C code

Cipher	Mode	CC	Inline	Unroll	Interleave	Schedule
DES	bitslice	Clang	✓	✓		✓
AES	bitslice	Clang	✓	✓		✓
	hslice	Clang	✓	✓		✓
Rectangle	bitslice	ICC	✓	✓		✓
	hslice	GCC			✓	✓
	vslice	Clang			✓	✓
Chacha20	vslice	ICC	✓	✓		✓
Serpent	vslice	Clang		✓	✓	

Take-aways

Satisfying:

- Simple programming model / language
- Compiler exploits these invariants
- Correctness: equivalence of combinational circuits
- Itself a back-end for further transformations

(aggregated bitslice model)

Disappointing:

- Do we really need a language for that?
- How to achieve economies of scale?
- How to interact with sequential code?

(e.g.: crypto runtime)

Our roadmap

1. Develop bitslicing as a programming model
 - Protection against faults
 - Protection against side-channels
2. Take back control!
 - Custom register allocation
 - Bypass C / target Jasmin
 - End-to-end correctness proof *(without the chains)*
3. Beyond data parallelism
 - Factor in the crypto runtime
 - Target embedded devices
 - Find a suitable host
4. ???
5. Turing award *(see Patterson & Hennessy's lecture)*